



## Computers and networks

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Monografía

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**Contenido:** Computers and networks -- Legal -- Contents -- Prologue -- Part I -- Chapter 1 -- 1.1 Concept of computer and digital -- 1.2 Binary codes -- 1.3 Logic data -- 1.4 Positional system -- 1.5 Unsigned numbers -- 1.6 Signed numbers -- 1.7 Real numbers -- 1.8 Characters -- Chapter 2 -- 2.1 Basic Concepts -- 2.2 Combinational -- 2.3 Sequential digital -- Part II -- Chapter 3 -- 3.1 Specific and generic -- 3.2 Design of the computer -- 3.3 Theoretical Computer -- Chapter 4 -- 4.1 Instructions -- 4.2 Operands -- 4.3 Assignment sentences -- 4.4 Arithmetic and logic sentences -- 4.5 Conditional sentences -- 4.6 Loops -- 4.7 Procedures -- Chapter 5 -- 5.1 Processor and program -- 5.2 Conventional machine -- 5.3 Microarchitecture level -- 5.4 The control unit -- Chapter 6 -- 6.1 Address space -- 6.2 Memory devices -- 6.3 Types of memory -- 6.4 Memory map -- 6.5 Creating the memory -- Chapter 7 -- 7.1 Connecting peripherals -- 7.2 Peripheral device -- 7.3 Synchronization through -- 7.4 Synchronization with interrupts -- Part III -- Chapter 8 -- 8.1 Computer network -- 8.2 Protocol architectures -- Chapter 9 -- 9.1 Physical layer -- 9.2 Data link layer -- 9.3 Connecting devices to a local area -- Chapter 10 -- 10.1 Network -- 10.2 Transport layer -- 10.3 Application layer -- Annexes -- Appendix A -- Bibliography

**Detalles del sistema:** Forma de acceso: World Wide Web

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